IN THE CLAIMS:

1. (Previously Presented) A liquid crystal display device, comprising:

a plurality of gate lines and data lines crossing each other to define a plurality of pixel regions;

a plurality of thin film transistors, each disposed in one of the pixel regions, each thin film transistor including:

a gate electrode on a first substrate;

a gate insulating layer over the first substrate;

a semiconductor layer on the gate insulating layer; and

source/drain electrodes on the semiconductor layer;

a passivation layer over the first substrate including the source/drain electrodes of the thin film transistors;

a plurality of pixel electrodes, each disposed in one of the pixel regions;

at least one Ti layer on at least one layer of the gate electrode and the source/drain electrodes of the thin film transistors; and

a TiO_2 masking layer formed in at least one of the thin film transistor or an at least one of the passivation layer and the pixel electorde.

2. (cancelled)

3. (Previously Presented) The device according to claim 1, wherein the TiO_2 masking layer is formed on at least the passivation layer of the thin film transistor.

4. (Previously Presented) The device according to claim 3, wherein a surface of the TiO_2 masking layer has hydrophilic properties.

5-6. (cancelled)

7. (Original) The device according to claim 1, further comprising:

a black matrix on a second substrate;

a color filter layer on the second substrate; and

a liquid crystal material layer between the first and second substrates.

8. (Previously Presented) The device according to claim 1, wherein the TiO_2 masking layer formed on at least each of the pixel electrodes.

9. (Previously Presented) The device according to claim 8, wherein a surface of the TiO_2 masking layer has hydrophilic properties.

10. (Previously Presented) The device according to claim 1, wherein at least one TiO_2 masking layer is formed in each of the thin film transistors.

11. (Previously Presented) The device according to claim 10, wherein a surface of each TiO_2 masking layer has hydrophilic properties.

12. (Currently Amended) A liquid crystal display device, comprising:

a plurality of gate lines and data lines crossing each other to define a plurality of pixel regions;

a thin film transistor in each pixel region a thin film transistor having a gate electrode, a semiconductor layer, source and drain electrodes; and

at least one layer of Ti layer and TiO2 layer on the at least one surface of the gate electrode, the semiconductor layer and the source and drain electrodes ~~a metal masking layer in~~ of the thin film transistor.

13. (Cancelled)

14. (Currently Amended) The device according to claim 12, wherein the ~~metal masking layer includes a Ti layer, and a~~ TiO_2 layer has ~~having~~ a hydrophilic surface.

15-70. (Cancelled)

71. (Cancelled)

72. (Currently Amended) A liquid crystal display device, comprising:

a plurality of gate lines and data lines crossing each other to define a plurality of pixel regions;

a plurality of thin film transistors, each disposed in one of the pixel regions, the thin film transistor including:

a gate electrode on a first substrate;

a gate insulating layer over the first substrate;

a semiconductor layer on the gate insulating layer; and

source/drain electrodes on the semiconductor layer;

a passivation layer over the first substrate including the source/drain electrodes of the thin film transistors;

a plurality of pixel electrodes, each disposed in one of the pixel regions;

at least one Ti layer on the at least one surface of the gate electrode, ~~on~~ the semiconductor layer, and the source and drain electrodes; and

a TiO_2 layer on at least one of the whole surface areas of the passivation layer ~~of the thin film transistor~~ or the pixel electrode.